Uni-directional 4.8V High Capacitance TVS

Description

The PTVSHC3D4V8UA Transient Voltage Suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's.

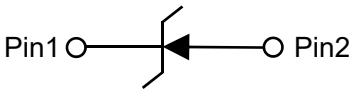
They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs.

The PTVSHC3D4V8UA protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC3D4V8UA is available in a SOD-323 package with working voltages of 4.8 volt.

SOD-323(Top View)

Feature

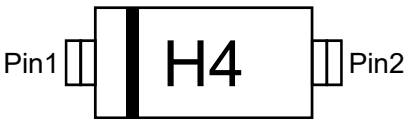
- 2600W Peak pulse power per line (t_P = 8/20μs)
- SOD-323 package
- Response time is typically < 1 ns
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to
  IEC 61000-4-2(ESD) ±30kV(air), ±30kV(contact);
  IEC 61000-4-4 (EFT) 40A (5/50ns)
  IEC 61000-4-5 (Lightning) 170A (8/20us)



Circuit Diagram

Applications

- Power Management
- Industrial Application
- Power Supply Protection
- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Peripherals



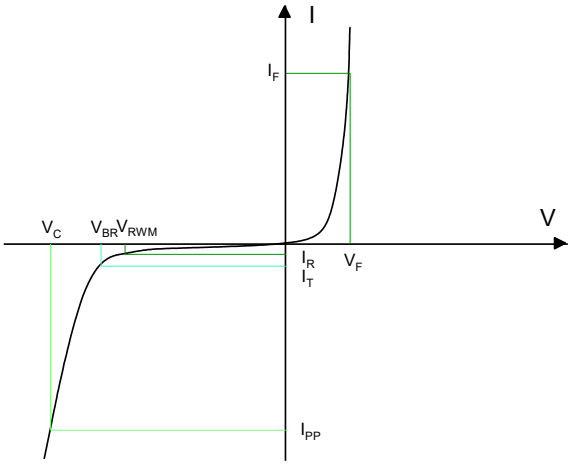
Marking (Top View)

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260℃
- Pure tin plating: 7 ~ 17 um

Electronics Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	V_{RWM}	-	-	-	4.8	V
Breakdown Voltage	V_{BR}	I_t = 1mA	5	5.9	6.5	V
Reverse Leakage Current	I_R	V_{RWM} = 4.8V	-	-	5	μA
Clamping Voltage	V_C	I_{PP} = 100A,t_P = 8/20μs	-	10.5	12	V
Clamping Voltage	V_C	I_{PP} = 170A,t_P = 8/20μs	-	14.5	16	V
Junction Capacitance	C_J	V_R = 0V,f = 1MHz	-	550	650	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t_P = 8/20μs)	P_{PP}	2600	W
Peak Pulse Current (t_P = 8/20μs)	I_{PP}	170	A
Lead Soldering Temperature	T_L	260 (10 sec)	℃
Junction and Storage Temperature Range	T_J,T_{STG}	-55~+150	℃

Typical Characteristics

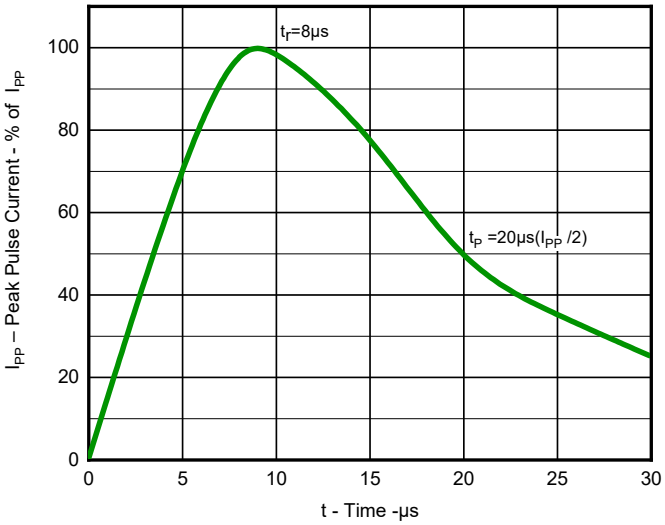


Fig 1.Pulse Waveform(8/20μs)

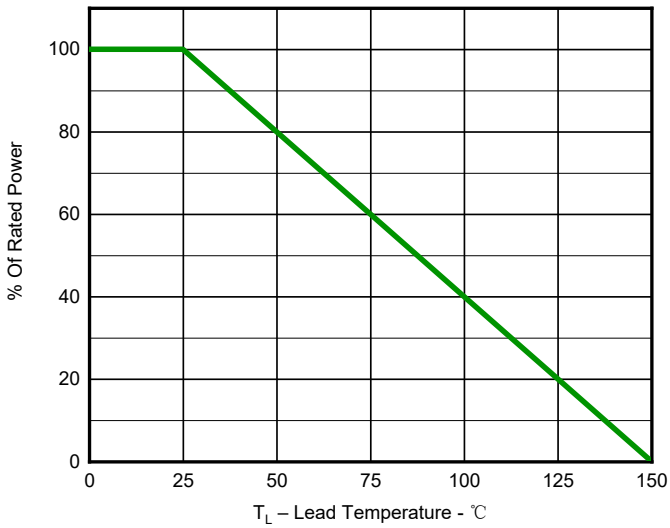


Fig 2.Power Derating Curve

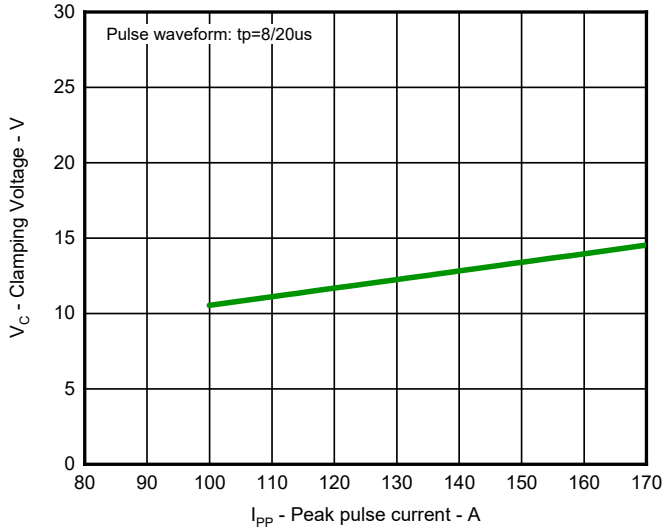


Fig 3. Clamping voltage vs. Peak pulse current

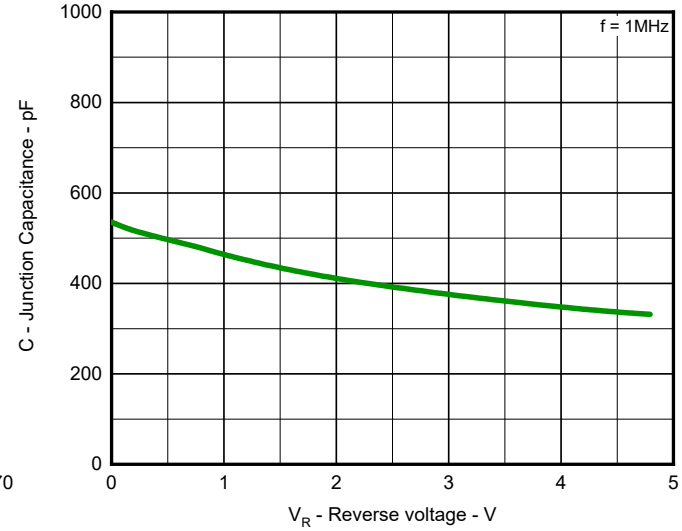


Fig 4. Capacitance vs. Reveres voltage

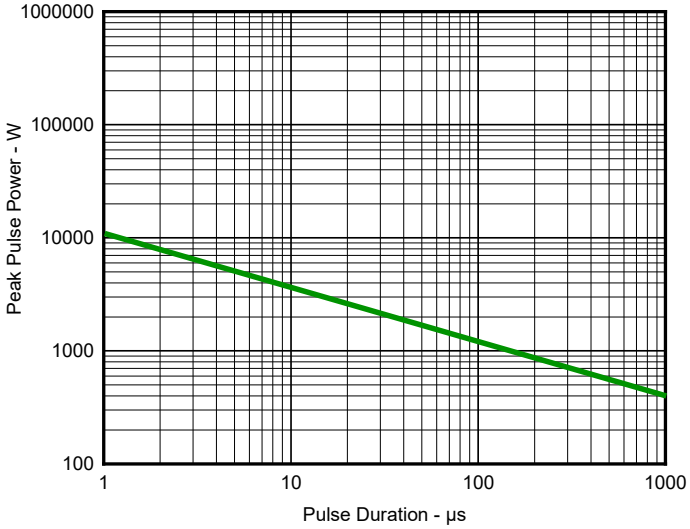
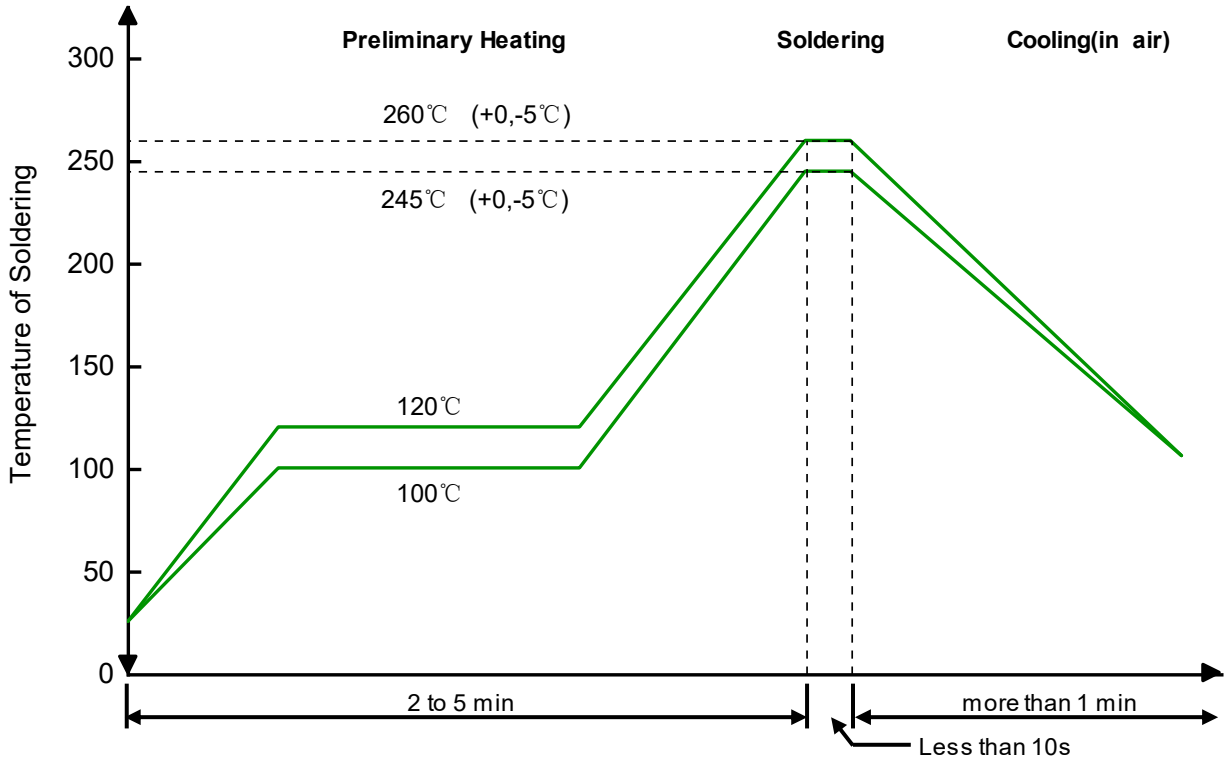


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

Solder Reflow Recommendation



Remark: Pb free for 260℃; Pb for 245℃.

PCB Design

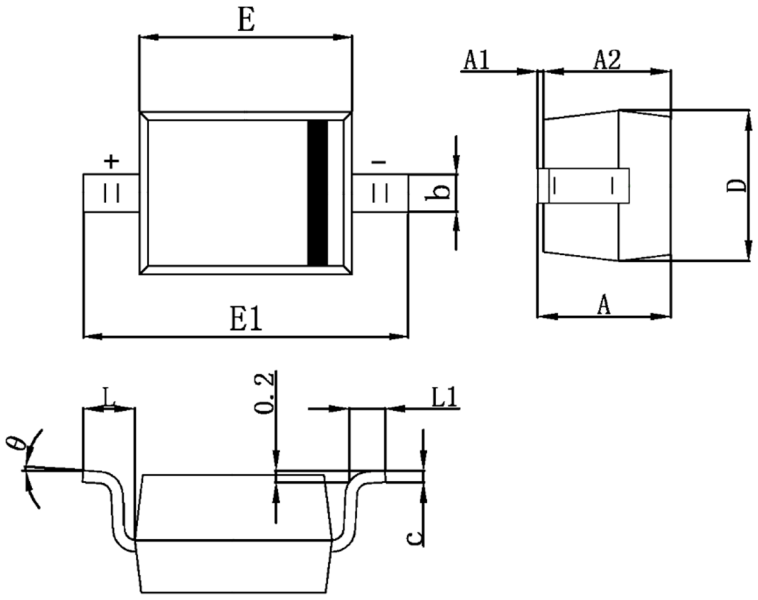
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

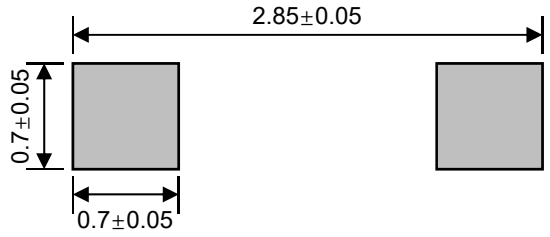
Ordering information

Device	Package	Reel	Shipping
PTVSHC3D4V8UA	SOD-323 (Pb-Free)	7"	3000 / Tape & Reel

Product dimension (SOD-323)



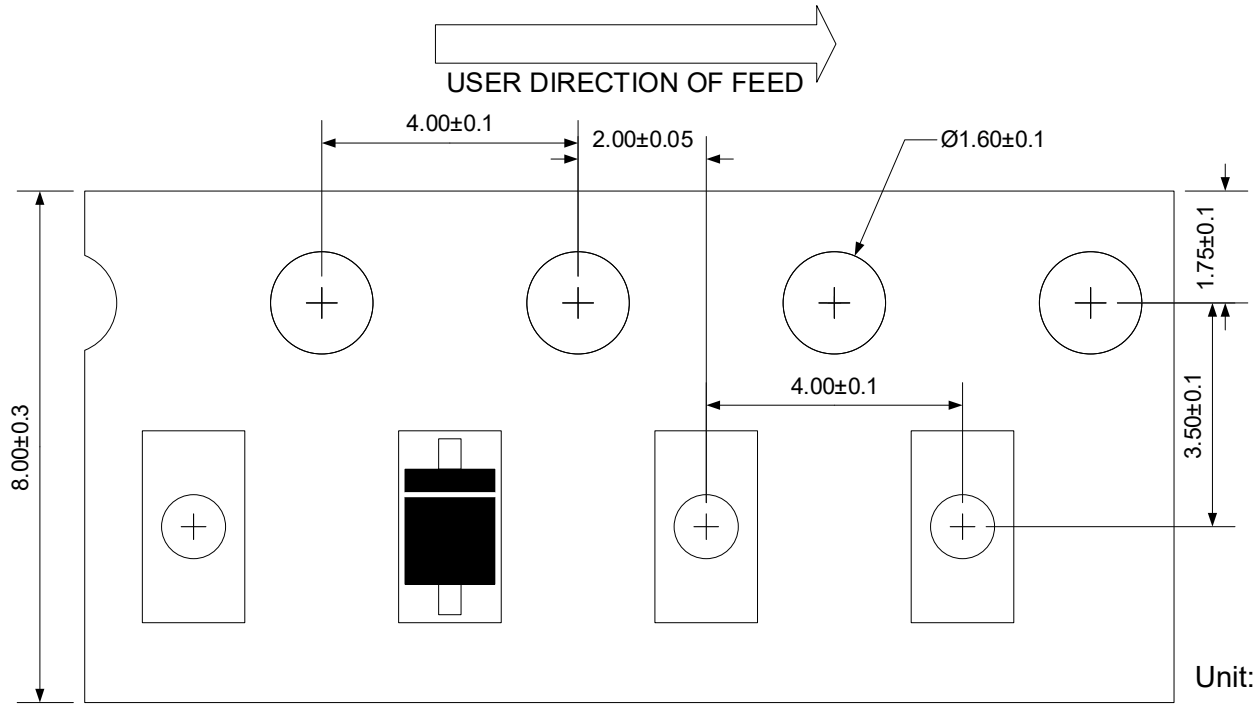
Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.031	0.039
A1	0.000	0.100	0.000	0.004
A2	0.850	0.950	0.033	0.037
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	1.250	1.450	0.049	0.057
E	1.600	1.800	0.063	0.071
E1	2.500	2.700	0.098	0.106
L	0.475 Ref.		0.019 Ref.	
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°



Unit:mm

Suggested PCB Layout

Load with information



Unit:mm

IMPORTANT NOTICE

and Prisemi® are registered trademarks of **Prisemi Electronics Co., Ltd** (Prisemi**),** Prisemi reserves the right to make changes without further notice to any products herein. Prisemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Prisemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. “Typical” parameters which may be provided in Prisemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including “Typicals” must be validated for each customer application by customer’s technical experts. Prisemi does not convey any license under its patent rights nor the rights of others. The products listed in this document are designed to be used with ordinary electronic equipment or devices, Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

Website: http://www.prisemi.com
For additional information, please contact your local Sales Representative.