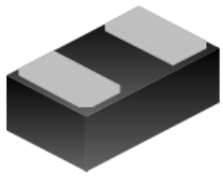
PESDNC2FD7VB

Bi-directional 7V Normal Capacitance ESD Protector

Description

The PESDNC2FD7VB protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L(Bottom View)

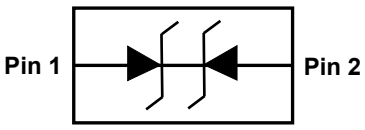
Feature

- 110W peak pulse power per line (t_P = 8/20μs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to
  IEC61000-4-2(ESD）±30kV(air), ±30kV(contact);
  IEC61000-4-4 (EFT) 40A (5/50ns)

Marking (Top View)

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies



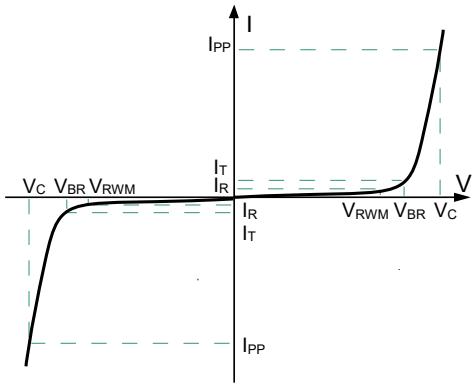
Circuit Diagram

Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260℃
- Device meets MSL 1 requirements
- DFN1006-2L without plating

Electronics Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Peak Reverse Working Voltage	V_{RWM}				7	V
Breakdown Voltage	V_{BR}	I_T = 1mA	7.5		10.5	V
Reverse Leakage	I_R	V_{RWM}= 7V			1.0	μA
Clamping Voltage(1)	V_C	TLP=16A, t_p=100ns		17		V
Dynamic Resistance(1)	R_{DYN}			0.35		Ω
Clamping Voltage(2)	V_C	I_{PP}=3A,t_p=8/20μs		11.5	14	V
		I_{PP}=8A,t_p=8/20μs		14.5	18	V
Junction Capacitance	C_J	V_R=0V f = 1MHz		21		pF

Notes:1. TLP parameter: Z0=50Ω,tp=100ns,tr=2ns,averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

2. Non-repetitve current pulse, according to IEC61000-4-5.

Absolute maximum rating@25℃

Rating	Symbol	Value	Unit
Peak Pulse Power (t_p=8/20μs)	P_{pp}	110	W
Peak Pulse Current(t_p=8/20μs)	I_{pp}	8	A
Operating Temperature	T_J	-55 to 150	℃
Storage Temperature	T_{STG}	-55 to 150	℃
ESD Protection-Contact Discharge	V_{ESD}	±30	kV
ESD Protection-Air Discharge	V_{ESD}	±30	kV

Typical Characteristics

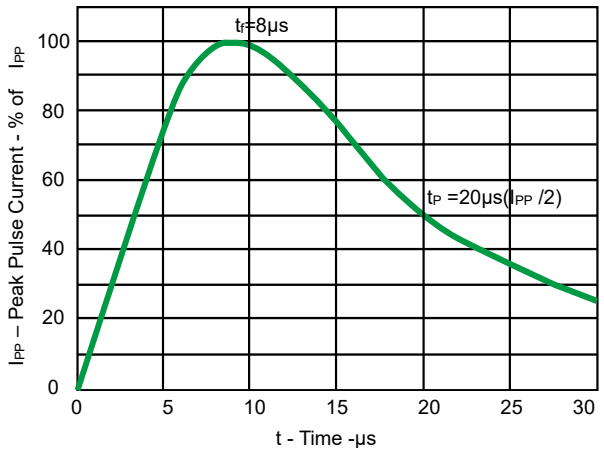


Fig 1. Pulse Waveform

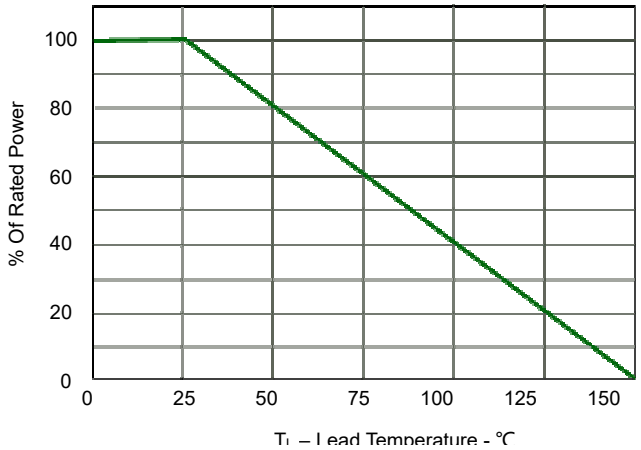


Fig 2. Power Derating Curve

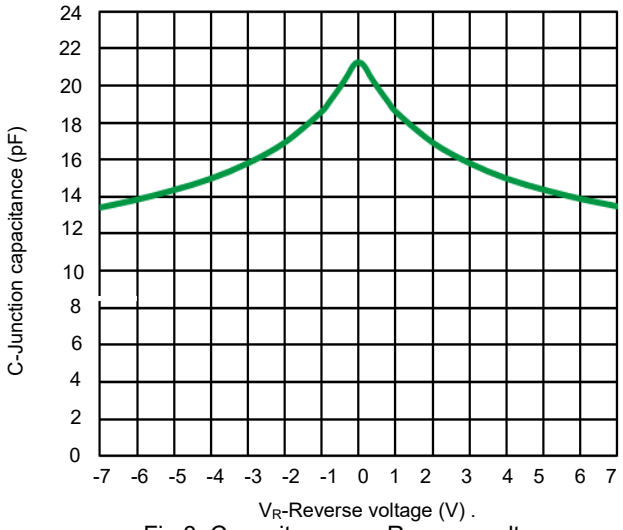


Fig 3. Capacitance vs. Reveres voltage

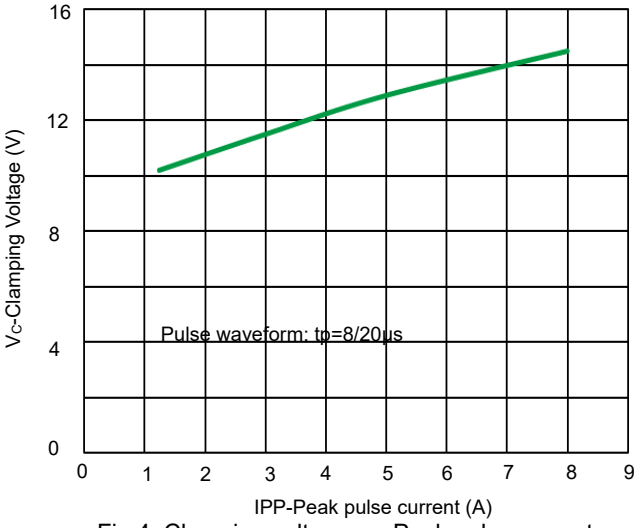


Fig 4. Clamping voltage vs. Peak pulse current

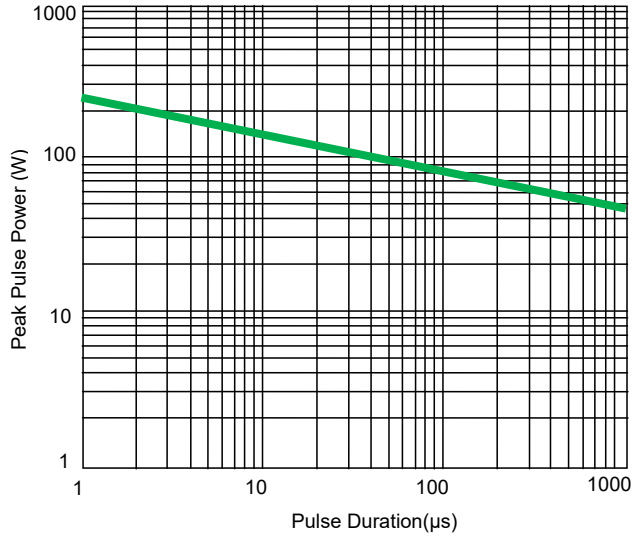


Fig 5. Non-Repetitive Peak Pulse Power vs. Pulse time

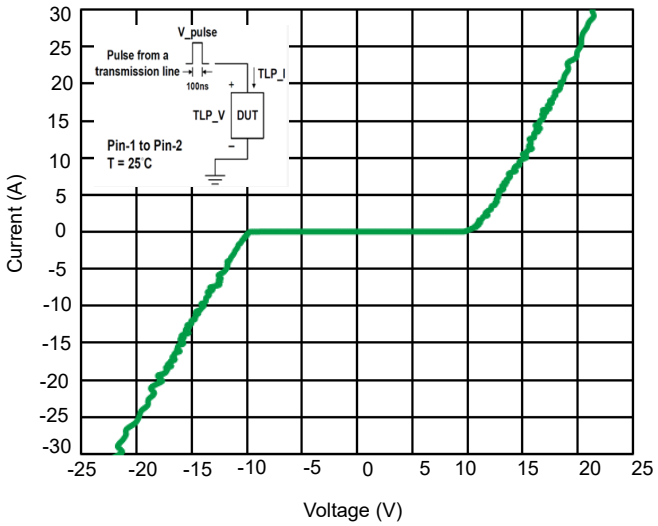


Fig 6. TLP Measurement

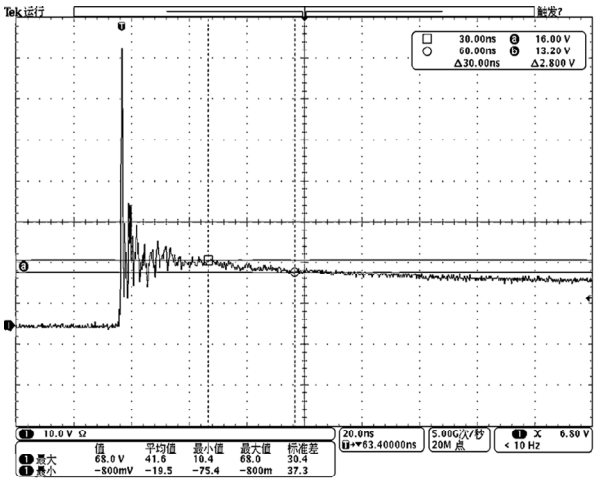


Fig 7. ESD clamping voltage
(IEC61000-4-2 +8kV contact)

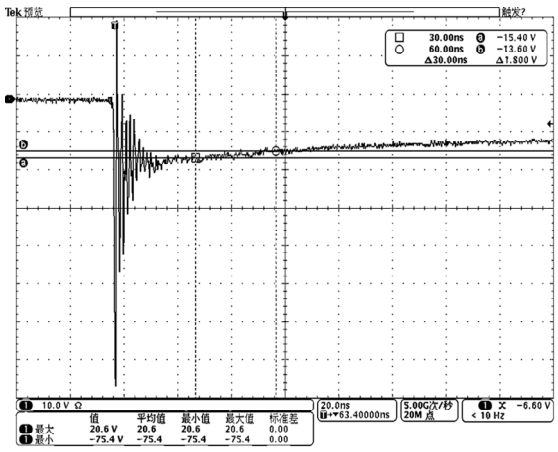
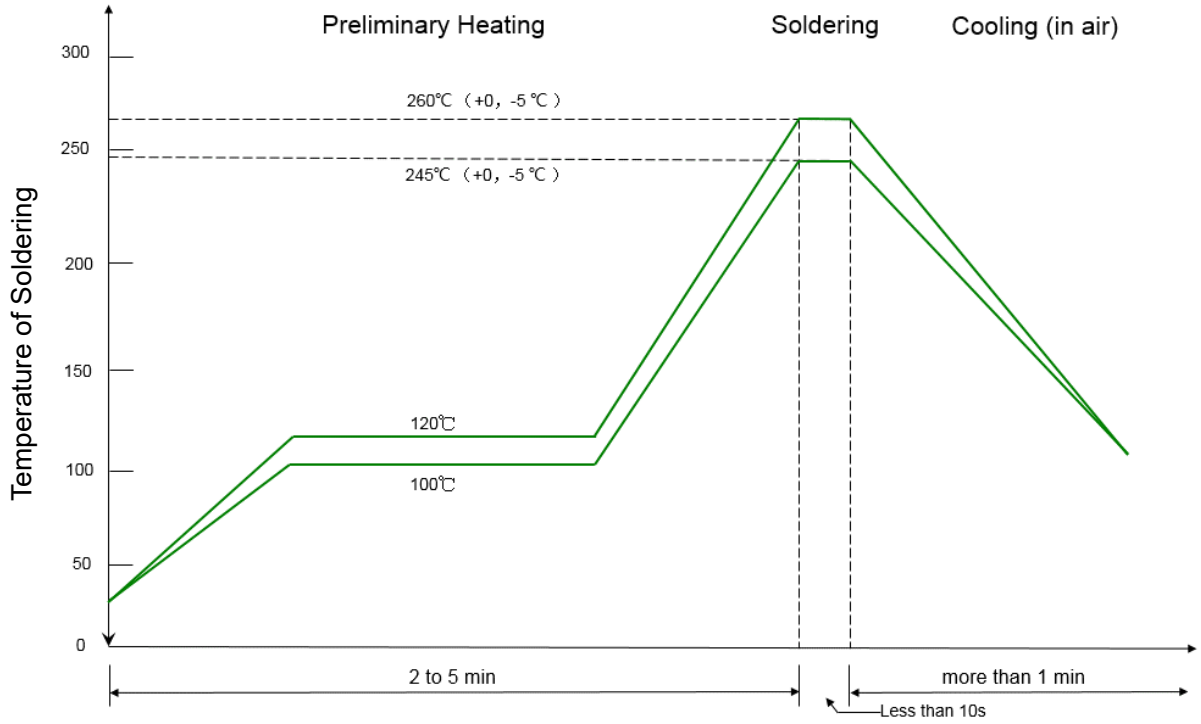


Fig 8. ESD clamping voltage
(IEC61000-4-2-8kV contact)

Solder Reflow Recommendation



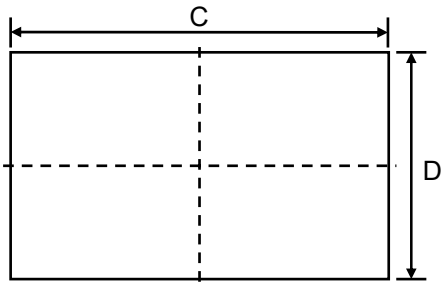
Remark: Pb free for 260℃； Pb for 245℃.

PCB Design

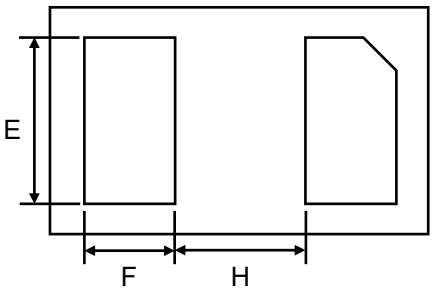
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

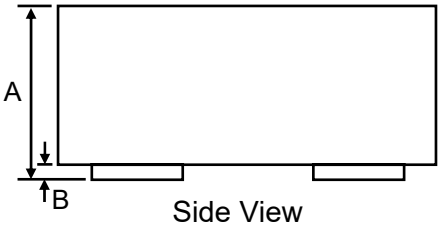
Product dimension (DFN1006-2L)



Top View

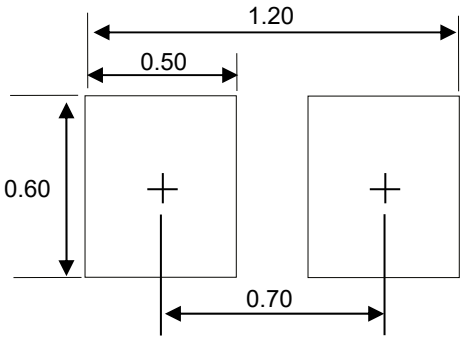


Bottom View



Side View

Dim	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.340	0.498	0.013	0.020
B	0.000	0.050	0.000	0.002
C	0.950	1.080	0.037	0.043
D	0.550	0.680	0.022	0.027
E	0.400	0.600	0.016	0.024
F	0.200	0.300	0.008	0.012
H	0.400 Typ.		0.015 Typ.	



Unit:mm

Suggested PCB Layout

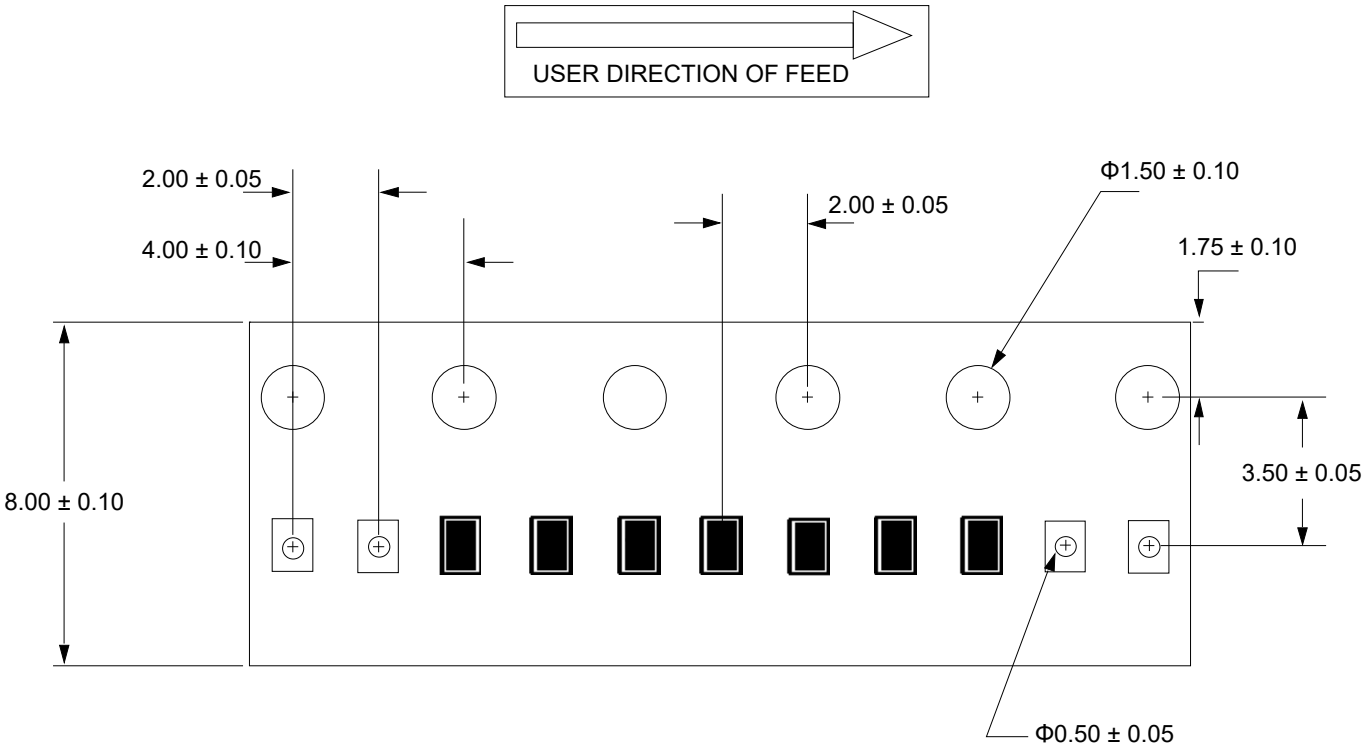
Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

Ordering information

Device	Package	Reel	MPQ
PESDNC2FD7VB	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

Load with information



Unit：mm

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